

FIG. 1

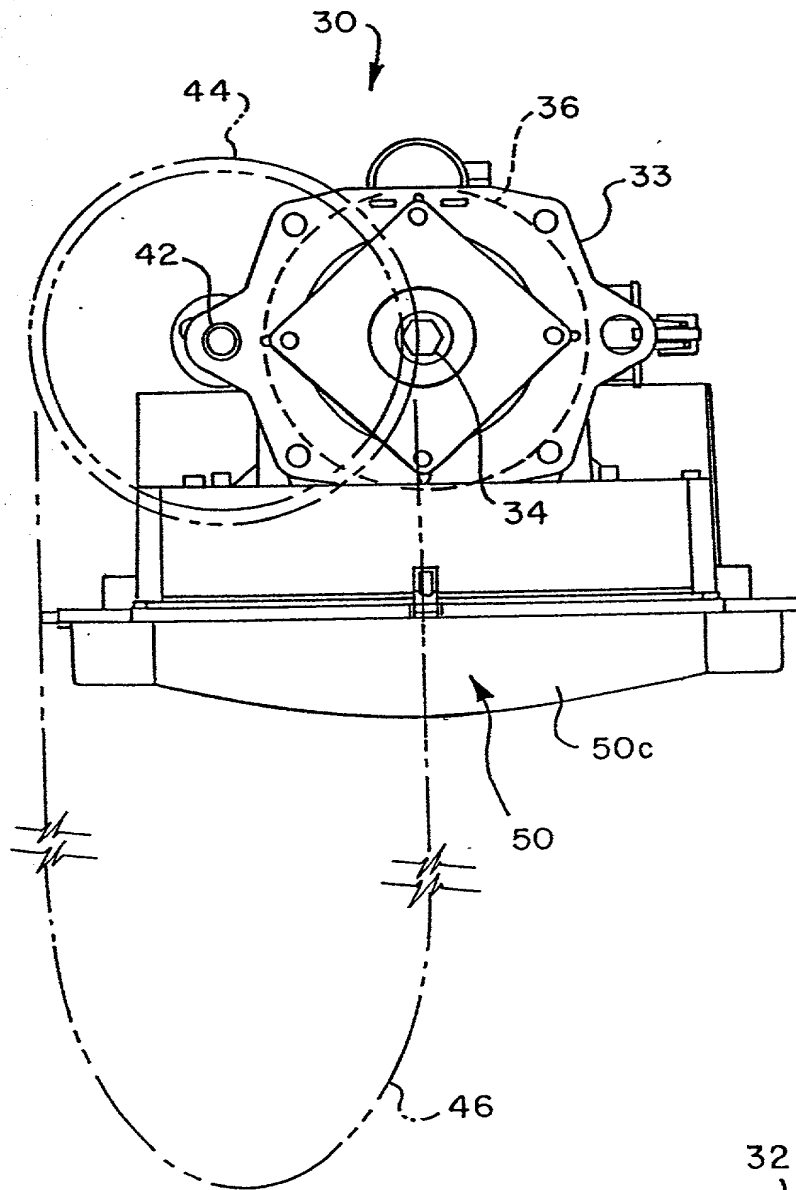


FIG. 2

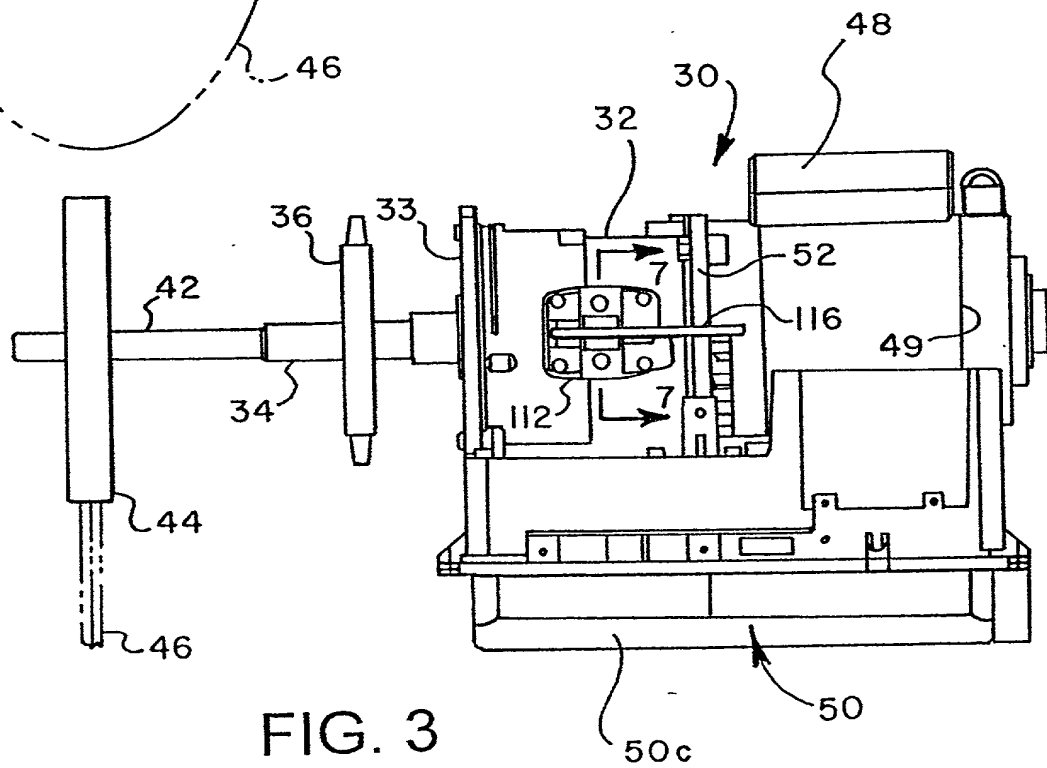


FIG. 3

FIG. 4

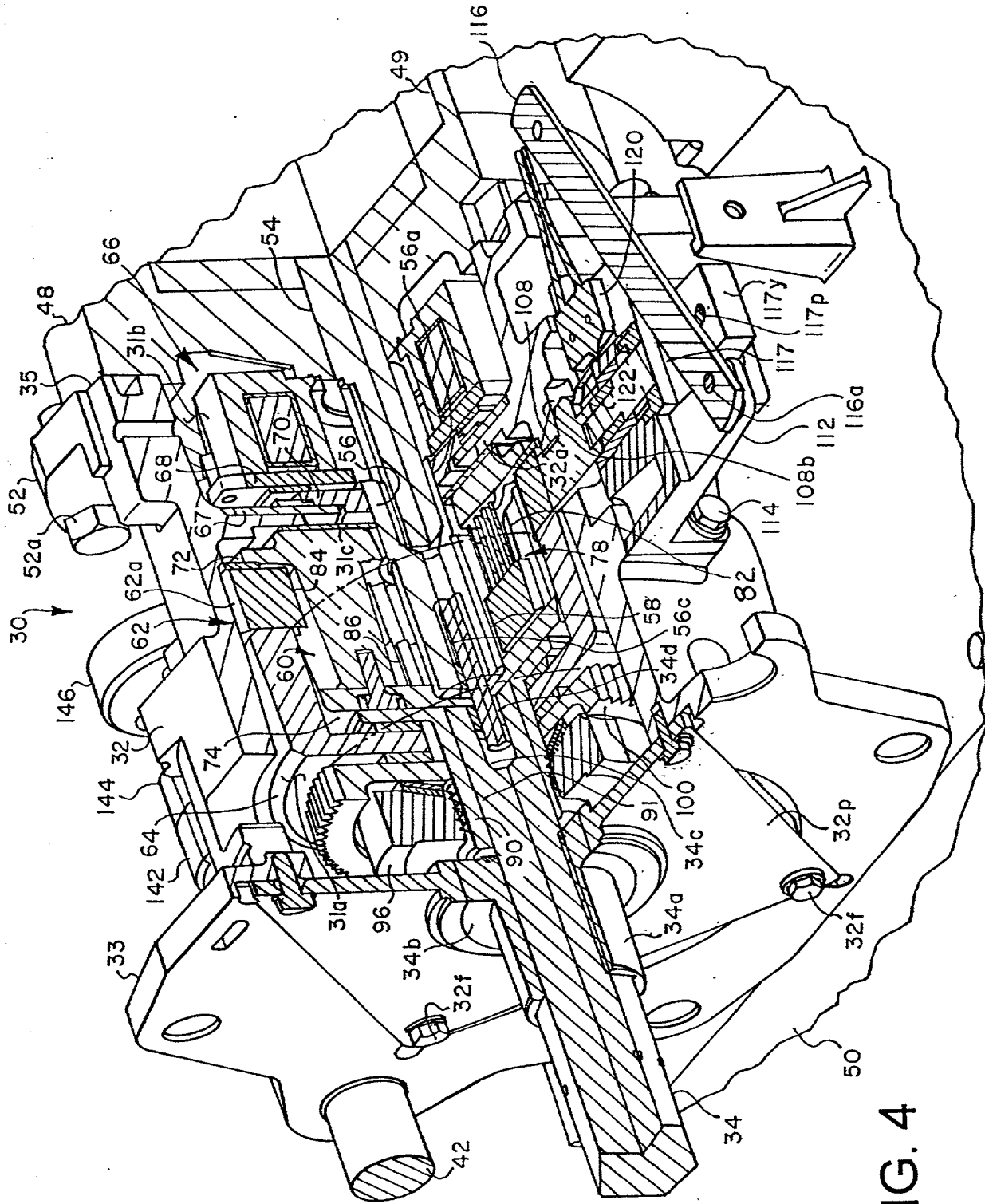


FIG. 4

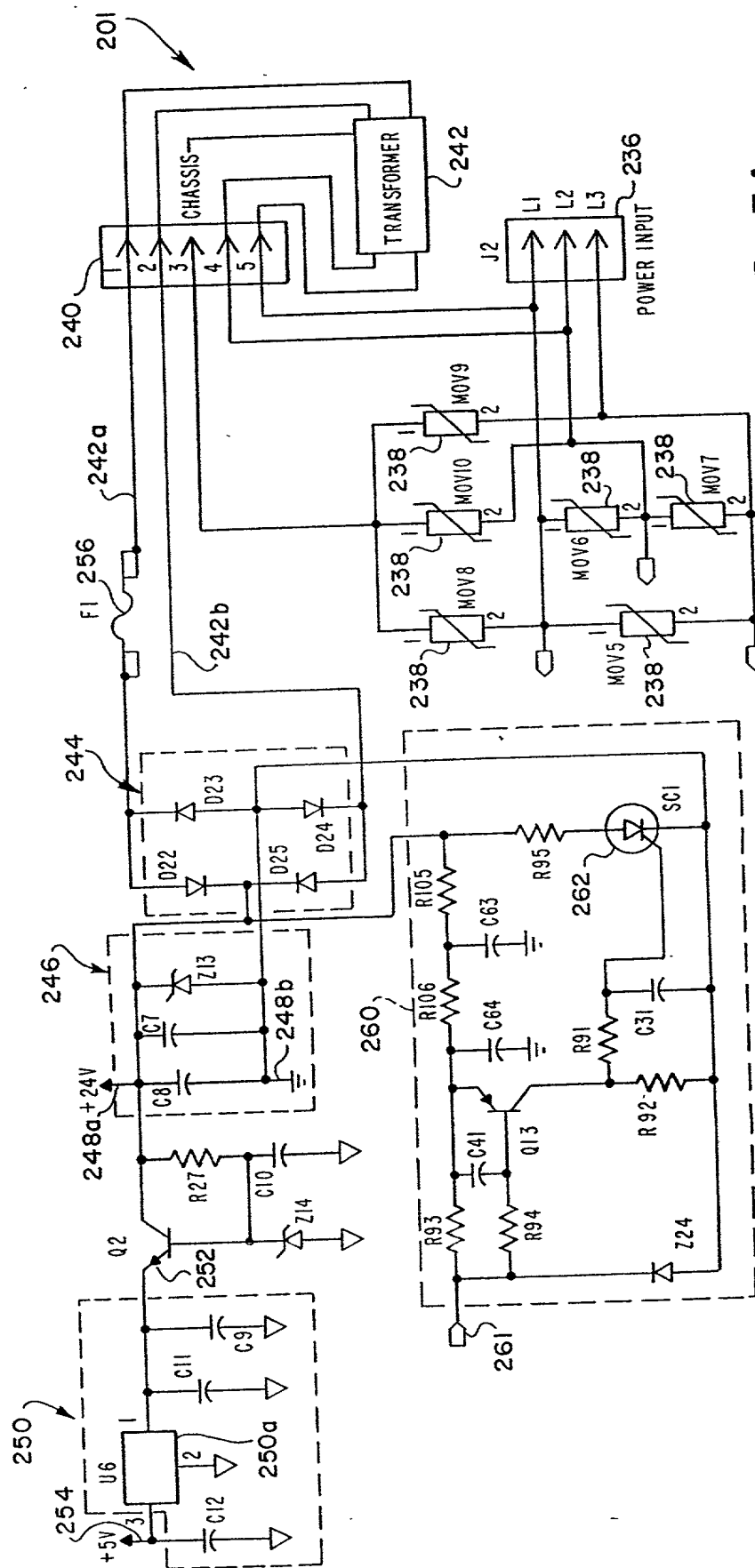
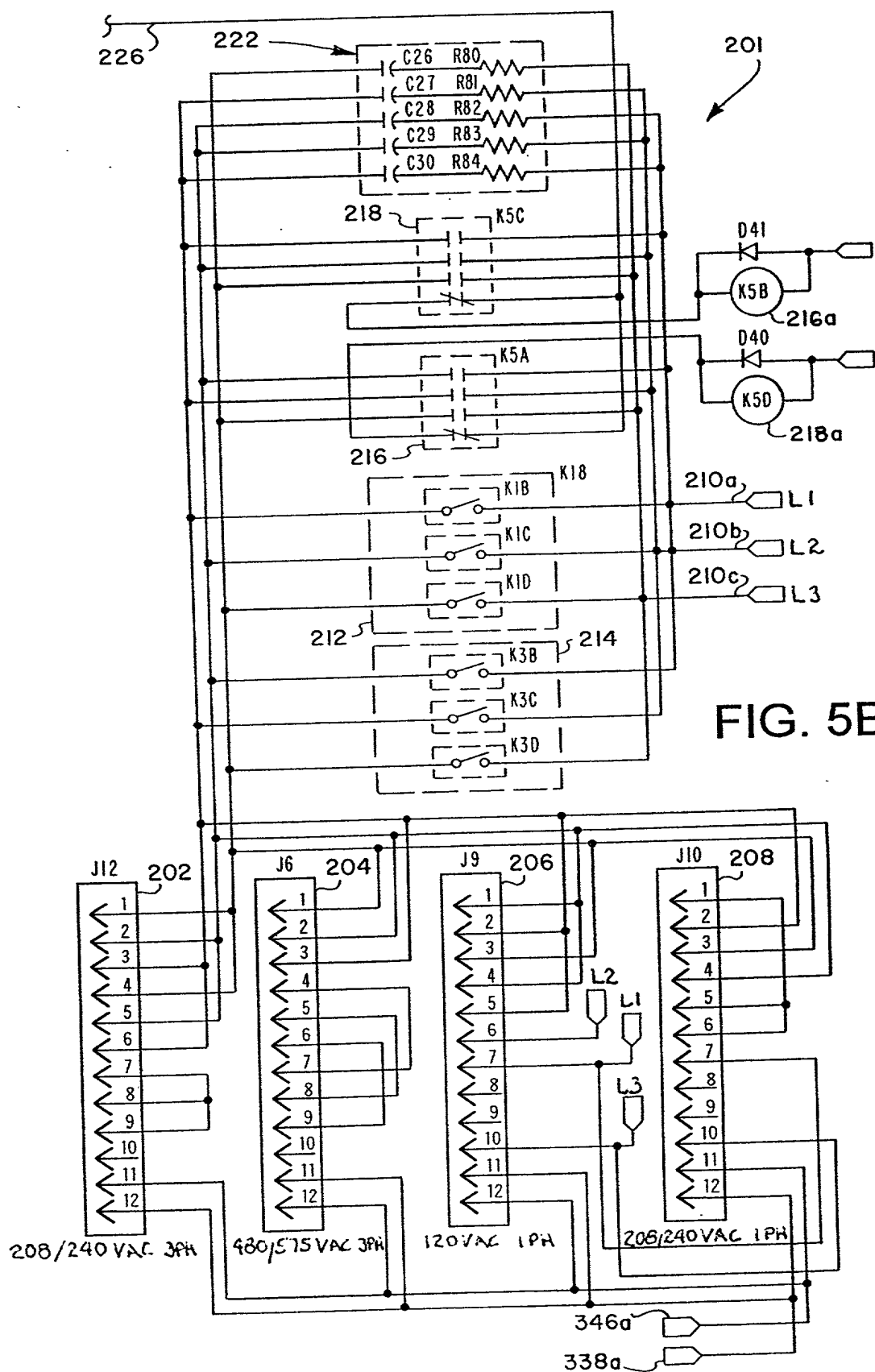


FIG. 5A



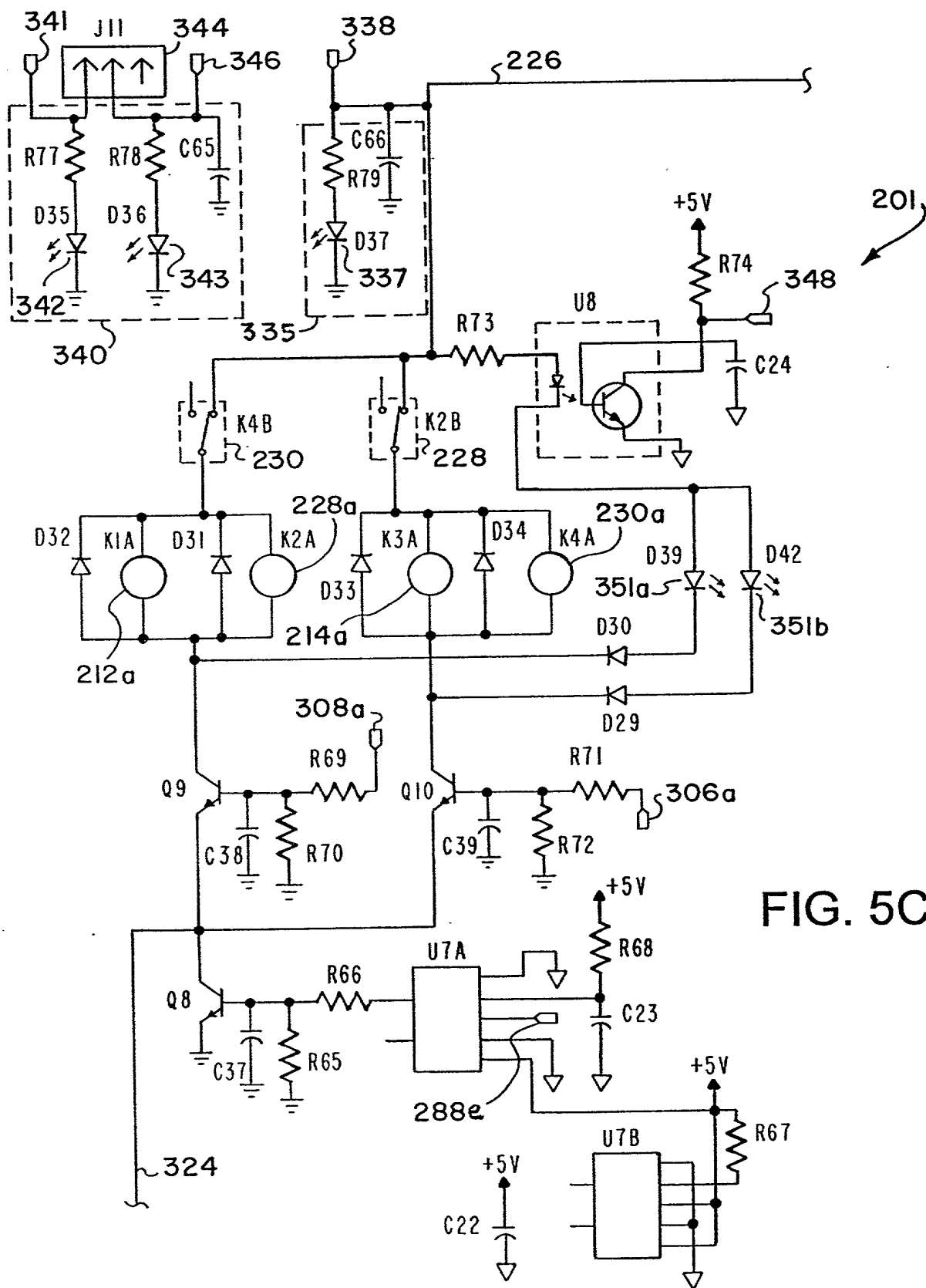
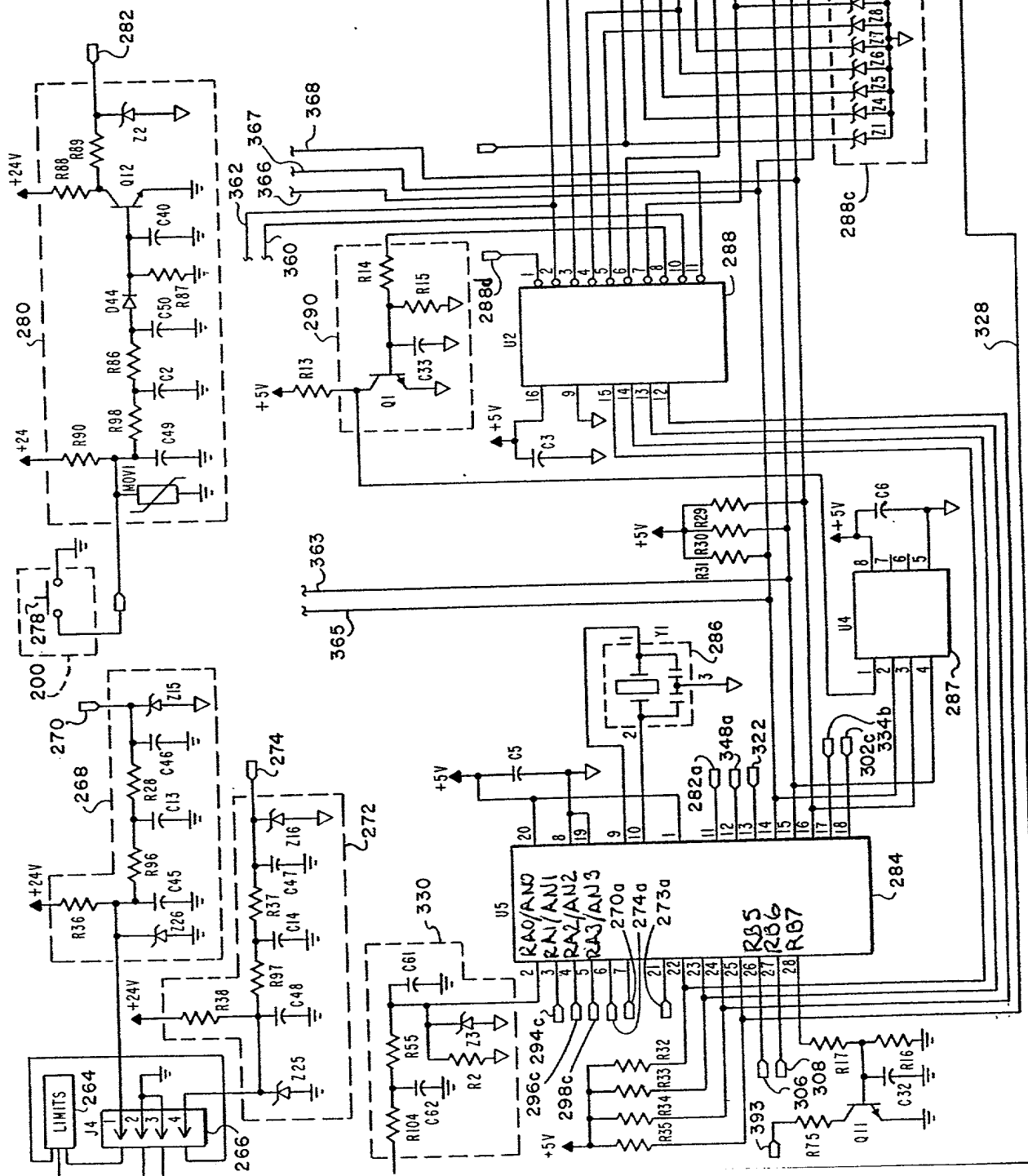


FIG. 5C

FIG. 5D



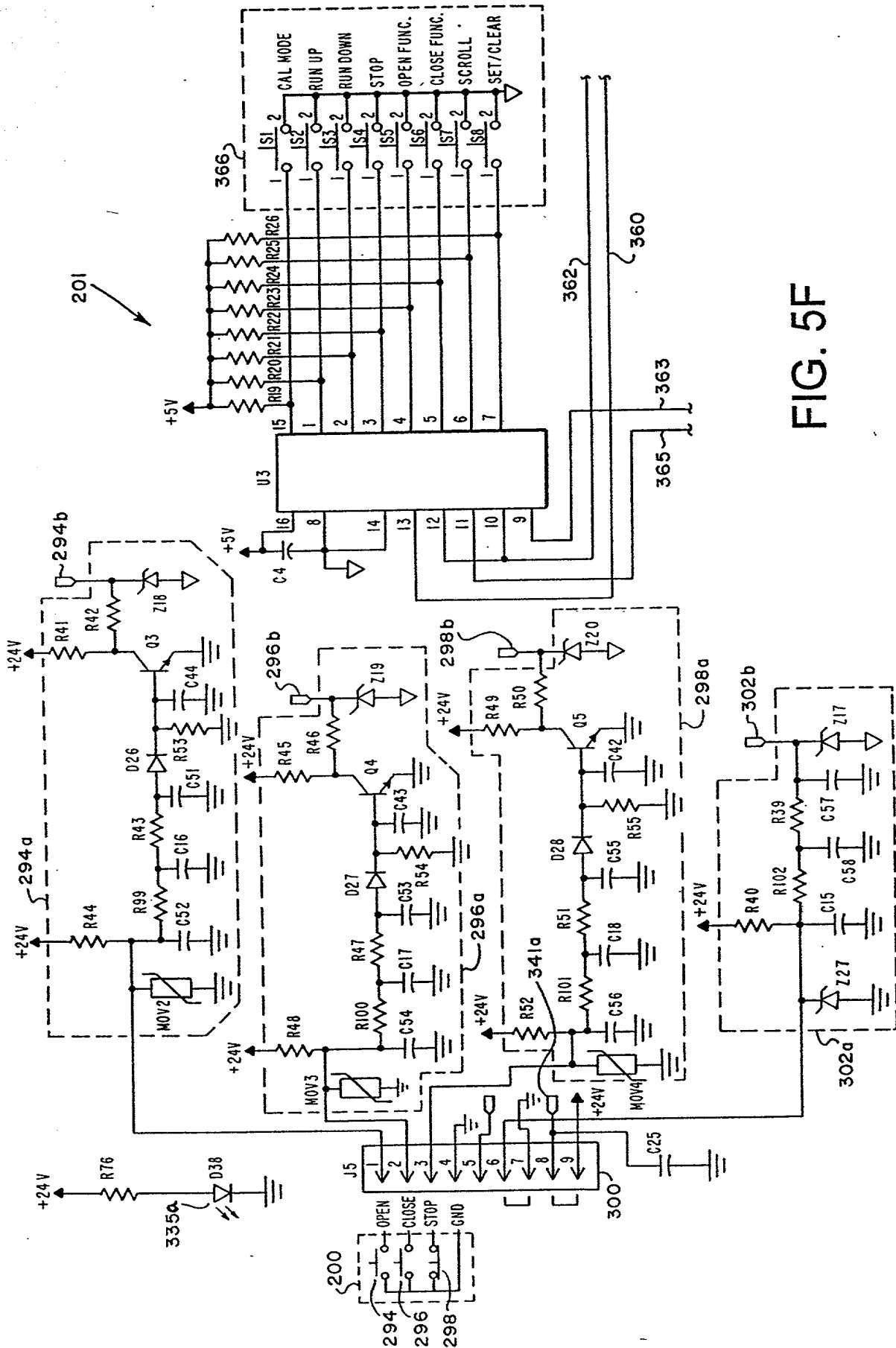


FIG. 5F

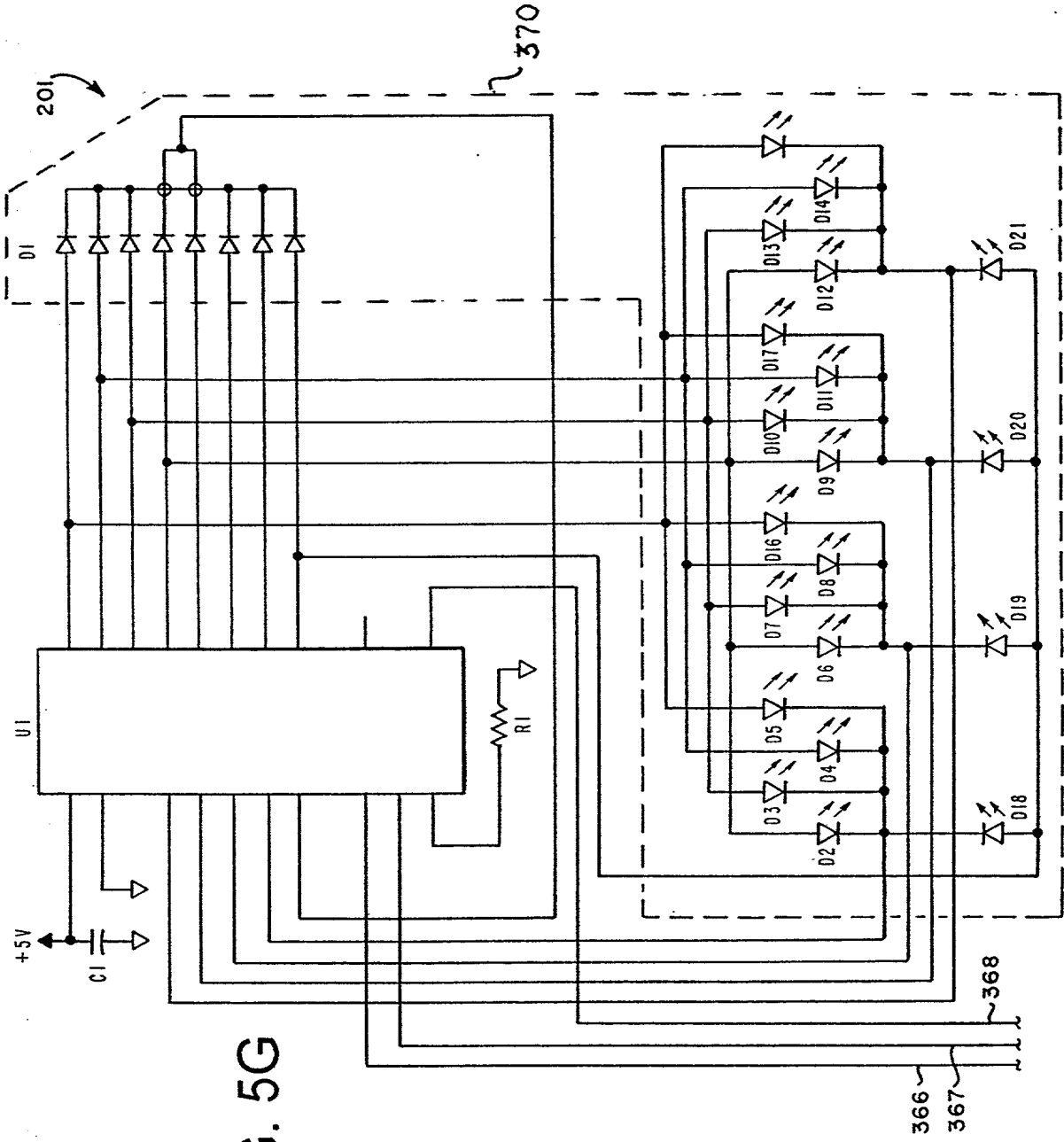


FIG. 5G

FIG. 5G is a schematic diagram of a multi-channel signal processing circuit.